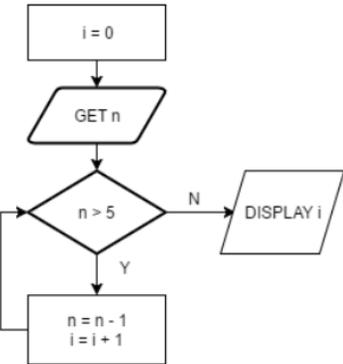
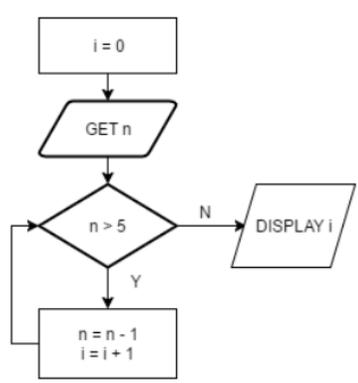




<p>How does optical storage technology work?</p>	<p>Which register is responsible for storing intermediate result from each calculation, at the end of each Fetch-Decode-Execute cycle?</p>	<p>What are the 4 layers of a network?</p>
<p>What will 'i' be if 'n' is assigned the value of 7?</p>  <pre>graph TD; A[i = 0] --&gt; B[/GET n/]; B --&gt; C{n &gt; 5}; C -- N --&gt; D[/DISPLAY i/]; C -- Y --&gt; E["n = n - 1 i = i + 1"]; E --&gt; C;</pre>	<p>Consider an array of elements "array = [5,4,3,2,1]" , what are the steps of insertions done while doing an insertion sort on the array?</p>	<p>How many colours can a 3 bit image hold?</p>

<p>How does optical storage technology work?</p> <p><b>A powerful laser burns 'pits' into the disc's shiny surface ('land'). A less powerful laser reads the data by shining onto the disc's surface. If the laser hits a pit, it doesn't reflect directly and is recognised as a 1. If the laser hits land, it reflects directly and is recognised as a 0. This way data (0/1) can be written to and read from a disc.</b></p>	<p>Which register is responsible for storing intermediate result from each calculation, at the end of each Fetch-Decode-Execute cycle?</p> <p style="text-align: center;">Accumulator</p>	<p>What are the 4 layers of a network?</p> <p style="text-align: center;"><b>Application Layer Transport Layer Network Layer Data Link and Physical Layer</b></p>
<p>What will 'i' be if 'n' is assigned the value of 7?</p>  <p style="text-align: center;"><b>2</b></p>	<p>Consider an array of elements "array = [5,4,3,2,1]", what are the steps of insertions done while doing an insertion sort on the array?</p> <p style="text-align: center;"><b>4 5 3 2 1    3 4 5 2 1    2 3 4 5 1    1 2 3 4</b> <b>5</b></p>	<p>How many colours can a 3 bit image hold?</p> <p style="text-align: center;"><b>8</b></p>