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"3-a-day" A-Level Exam Practice Unit 1 (022)

Question 1			
(a) An XOR gate is shown below. Complete the truth table for XOR.			
A B B Q			
	A B Q 1 1 1 0 0 1 0 0	[2]	
Question 2			
(b) A set of logic gates are connected and the set of logic gates are connecte	ected as below.	(i) Complete the Truth Table below: $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	
Question3			
answer.	nat the circuit does. You should ref		
		[4]	

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Answer 1	
Answer 1 A B Q 1 1 0 1 0 1 0 1 1 0 0 0 1 mark for the first two rows	AO1.2 2
1 mark for the last two rows	
Answer 2 A B Cin S Cout 1 1 1 1 1 1 1 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 0 1 1 0 0 1 0 1 0 1 1 0 1 0 0 1 1 0 1 0 0 1 1 0 0 0 0 0 0 1 1 0 0 0 0 1 Mark for rows 1 and 2 1 Mark for rows 3 and 4 1 Mark for rows 5 and 6 1 1 Mark for rows 7 and 8 1 Mark for rows 7 and 8 1	
 Answer 3 Circuit adds two bits (and a carry bit) together/is an adder. A B and C_{in} are added together The result is given in S And a carry bit in C_{out} (1 per -) 	A02.2 4